

Case No. 02GR119554482

COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

In re Application of: **JOET ET AL.**
Serial No.: **Not yet assigned**
Filed: **Herewith**
For: **METHOD AND DEVICE FOR GENERATING A SIGNAL WITH
A FREQUENCY EQUAL TO THE PRODUCT OF A
REFERENCE FREQUENCY AND A REAL NUMBER**
Sir:

Transmitted herewith is an INFORMATION DISCLOSURE STATEMENT in the above-identified application.

1. ☒ This IDS is submitted under 37 C.F.R. § 1.97. No fee is required.
2. ☐ This IDS is submitted under 37 C.F.R. § 1.97(c). Enclosed is a check in the amount of \$ 180.00.
3. ☐ This IDS is submitted under 37 C.F.R. § 1.97(c) and (e). No fee is required.
4. ☐ This IDS is submitted under 37 C.F.R. § 1.97(d) and (e). Enclosed is a check in the amount of \$ 130.00 to cover the petition fee.
5. ☒ The Commissioner is hereby authorized to charge or credit any discrepancies in fee amounts to Deposit Account No. 01-0484.
6. ☒ Please associate this application with Customer No. 27975.



27975

PATENT TRADEMARK OFFICE

Date: October 17, 2003


MICHAEL W. TAYLOR
Reg. No. 43,182

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
JOET ET AL.

Serial No. Not yet assigned

Filing Date: Herewith

For: METHOD AND DEVICE FOR
GENERATING A SIGNAL WITH A
FREQUENCY EQUAL TO THE PRODUCT
OF A REFERENCE FREQUENCY AND
A REAL NUMBER

)
) I HEREBY CERTIFY THIS PAPER OR FEE IS BEING
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) PATENT APPLICATION, PO BOX 1450,
) ALEXANDRIA, VA 22313-1450.

) EXPRESS MAIL NO: EV301516111US

) DATE OF DEPOSIT: October 17, 2003

) NAME: Justin Goree

) SIGNATURE: Justin Goree

CITATION UNDER 37 CFR \$1.97

COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

Sir:

Attached is Form PTO-1449 listing several references for consideration in the examination of the above-identified application. A copy of each reference is also enclosed. It is requested that these references be considered by the Examiner and officially made of record in accordance with the provisions of 37 CFR \$1.97 and Section 609 of the MPEP.

Respectfully submitted,

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INFORMATION DISCLOSURE STATEMENT			Atty Docket: Serial No.: Applicant: Filing Date: Group:		02GR119554482 Not Yet Assigned Joet et al. Herewith		
U.S. PATENT DOCUMENTS							
Examiner Initials		Document Number	Date	Name	Class	Sub Class	Filing Date
	AA	5,821,816	10/13/98	Patterson	331	1A	
	AB	2002/0140512	10/3/02	Stockton	331	11	4/3/01
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Sub Class	Translation
	AI						
	AJ						
	AK						
OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)							
	AL	Djemouai et al., New Frequency-Locked Loop Based on CMOS Frequency-to-Voltage Converter: Design and Implementation, IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 48, No. 5, May 2001, Pages 441-449, XP002247796					
	AM						
	AN						
	AO						
	AP						
EXAMINER:				DATE CONSIDERED:			

***EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.